# **DC-DC Converter SNE3 Series**



### 3W Unregulated Single output



#### Features

- 4 Pin SIL Package
- 3000 VDC Isolation
- Low Ripple and Noise
- Efficiency up to 91%

ABSOLUTE MAXIMUM RATINGS(5)

- -40 ~ 90°C Operation Temperature Range
- Non-Conductive Black Plastic Case
- EMI Complies With EN55032 Class B

he SNE3 series is a family of cost effective 3W single output DC-DC converters. These converters achieve low cost and ultra-miniature SIP 4 pin size. Devices are encapsulated using flame retardant resin. The models operate from input voltage of 5, 12, 24 Vdc with output voltage of 5, 9, 12, 15 Vdc. High performance features include 3000Vdc input/output isolation, high efficiency operation and output voltage accuracy of ±3% maximum. Standard features include an input range of ±10% tolerance and low output noise and ripple.

All specifications typical at Ta=25°C, nominal input voltage and full load unless otherwise specified

OUTPUT SPECIFICATIONS				
Output Voltage accuracy	±3% ,max.			
Line regulation	±1.2% / Per 1% Vin Change ,max.			
Load regulation	(From 10% to 100% Load) ±10%, max.			
Ripple & noise (20 MHz bandv	width)(1) 100mVpk-pk,max.			
Temperature coefficient	±0.02%/°C			
Canacitor load(2)	See table may			

These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability.		
Input Surge Voltage(100mS)		
5 Models	7 Vdc ,max.	
12 Models	15 Vdc ,max.	
24 Models	28 Vdc ,max.	
Soldering Temperature	260°C,max.	

INPUT SPECIFICATIONS	
Intput Voltage Range	±10% ,max.
Input Current(Full-Load)	See table ,typ.
Input Current(No-Load)	See table ,max.
Input Filter	Capacitors
Input Reflected Ripple Current(3)	20mApk-pk ,typ.
Start up Time	20mS, typ.
(Nominal Vin and constant resistive load)	

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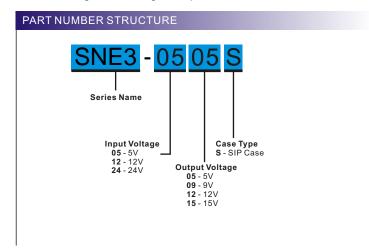
PHYSICAL SPECIFICATIONS				
Case Material	Non-conductive Black Plastic(UL94V-0 rated)			
Pin Material	C5191R-H Solder-coated			
Potting Material	Silicon (UL94V-0 rated)			
Weight	2.2g,typ.			
Dimensions	0.46"x0.29"x0.40"			

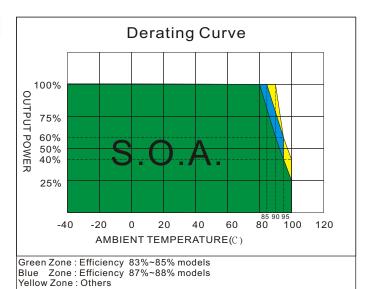
ENVIRONMENT SPECIFICATIONS		
Operating Temperature	-40°C~ +100°C(See Derating Curve)	
	$-40^{\circ}$ C ~ $+90^{\circ}$ C(For 100% Load)	
Maximum Case Temperature	115°C	
Storage Temperature	-55°C~125°C	
Cooling(4)	Nature Convection	

GENERAL SPECIFICATIONS	
Efficiency	See table ,typ.
I/O Isolation Voltage(tested for 60 sec)	3000Vdc
Input/Output	
I/O Isolation Capacitance	65 pF ,Max.
I/O Isolation Resistance	1000M Ohm
Switching Frequency	40~70kHz
Humidity	95% rel H
Reliability Calculated MTBF(MIL-HDBK-217 F)	>6.7Mhrs@25°C
Safety Standard: (designed to meet)	IEC/UL/EN 60950-1
	IEC/UL/EN 62368-1

EMC SPECIFICATIONS		
Conducted Emissions (6)	EN55032	CLASS B
Radiated Emissions	EN55032	CLASS B
ESD	IEC 61000-4-2	Perf. Criteria A
RS	IEC 61000-4-3	Perf. Criteria A
EFT (7)	IEC 61000-4-4	Perf. Criteria A
Surge (7)	IEC 61000-4-5	Perf. Criteria A
CS	IEC 61000-4-6	Perf. Criteria A
PFMF	IEC 61000-4-8	Perf. Criteria A

### SNE3 Unregulated Single output





### MODEL SELECTION GUIDE

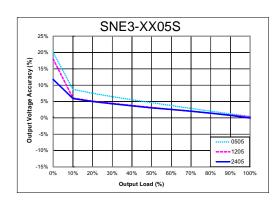
	INPUT	INPUT	Current	ОИТРИТ	OUTPUT Current	EFFICIENCY	Capacitor
MODEL NUMBER	Voltage Range	No-Load	Full Load	Voltage	Full load	@FL	Load @FL
	(Vdc)	(mA, max.)	(mA, typ.)	(Vdc)	(mA)	(%, typ.)	(uF, max.)
SNE3-0505S	5 (4.5 ~ 5.5)	50	723	5	600	83	3300
SNE3-0509S	5 (4.5 ~ 5.5)	60	690	9	333	87	1200
SNE3-0512S	5 (4.5 ~ 5.5)	55	682	12	250	88	1000
SNE3-0515S	5 (4.5 ~ 5.5)	60	682	15	200	88	820
SNE3-1205S	12 (10.8 ~ 13.2)	25	294	5	600	85	3300
SNE3-1209S	12 (10.8 ~ 13.2)	30	281	9	333	89	1200
SNE3-1212S	12 (10.8 ~ 13.2)	30	278	12	250	90	1000
SNE3-1215S	12 (10.8 ~ 13.2)	30	275	15	200	91	820
SNE3-2405S	24 (21.6 ~ 26.4)	15	147	5	600	85	3300
SNE3-2409S	24 (21.6 ~ 26.4)	15	141	9	333	89	1200
SNE3-2412S	24 (21.6 ~ 26.4)	15	139	12	250	90	1000
SNE3-2415S	24 (21.6 ~ 26.4)	15	138	15	200	91	820

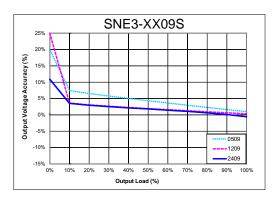
#### NOTE

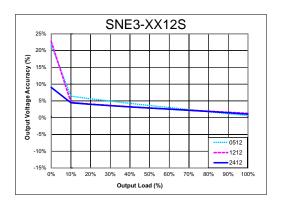
- 1. Ripple/Noise measured with a  $10\mu F$  electrolytic capacitor and  $0.1\mu F$  ceramic capacitor.
- 2. Tested by minimal Vin and constant resistive load.
- 3. Measured Input reflected ripple current with a simulated source inductance of  $12\mu H$  and a source capacitor  $Cin(10\mu F, ESR<1.0\Omega \text{ at } 100KHz)$ .
- 4. "Nature Convection" is usually about 30-65 LFM but is not equal to still air (0 LFM).
- 5. Exceeding the absolute ratings of the unit could cause damage. It is not allowed for continuous operating.
- 6. Input filter components are be required to help meet conducted emission class B, which application refer to the EMI Filter of design & feature configuration.
- 7. An external filter capacitor is required if the module has to meet IEC61000-4-4 and IEC61000-4-5. The filter capacitor SCHMID-M suggest: two electrolytic capacitors (Ruby-con BXF series, 100µF/250V) in parallel
- 8. Operation under no-load conditions will not damage these devices, however they may not meet all listed specifications.

### DERATING CURVE AND EFFICIENCY vs OUTPUT CURRENT CURVE

# Accuracy vs. Load

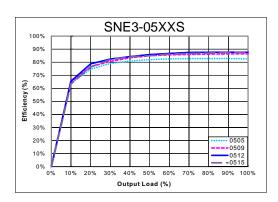


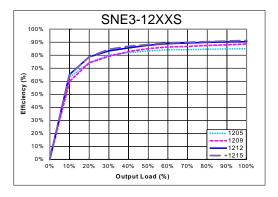


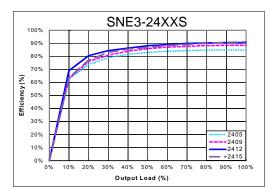




# Efficiency vs. Load



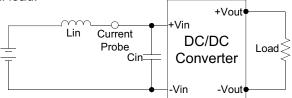




#### **TEST CONFIGURATIONS**

# Input Reflected Ripple Current Test Step

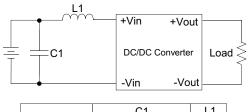
Input reflected ripple current is measured through a source inductor Lin( $12\mu H$ ) and a source capacitor Cin( $10\mu F$ , ESR< $1.0\Omega$  at 100kHz) at nominal input and full load.



### **EMI Filter(Conducted Emissions)**

Input filter components (C1,L1) are used to help meet conducted emissions requirement for the module.

These components should be mounted as close as possible to the module; and all leads should be minimized to decrease radiated noise.

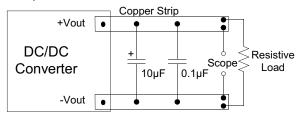


	C1	L1
SNE3-05XXS	1206,2.2µF,50V	2.2µH
SNE3-12XXS SNE3-24XXS	1206,4.7µF,50V	4.7µH

### **Output Ripple & Noise Measurement Test**

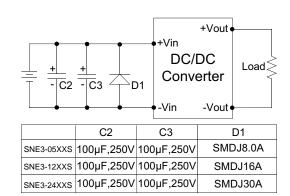
Use a  $10\mu F$  electrolytic capacitor and  $0.1\mu F$  ceramic capacitor.

The Scope measurement bandwidth is 20MHz.

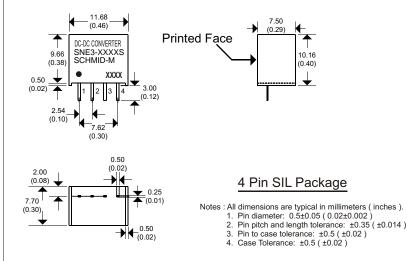


### **EFT/Surge Filter**

Input components (C2、C3、D1) are used to help meet IEC61000-4-4 and IEC61000-4-5.



#### MECHANICAL SPECIFICATIONS



PIN CONNECTIONS		
PIN NUMBER	SINGLE	
1	-V Input	
2	+V Input	
3	-V Output	
4	+V Output	