

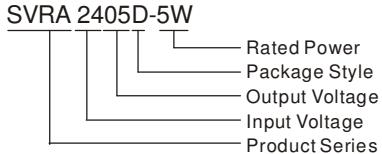


## SVRA\_D-5W & SVRB\_D-5W Series

**5W, 2:1 WIDE INPUT, ISOLATED & REGULATED  
DUAL/SINGLE OUTPUT DC-DC CONVERTER**



### PART NUMBER SYSTEM



### FEATURES

- Efficiency up to 85%
- 1.5KVDC Isolation
- Operating temperature: -40°C ~ +85°C
- Metal shielding package
- Industry standard pinout
- Good Performance Under Both High and Low Temperature, Meet Industrial Technical Requirements

### APPLICATIONS

The SVRA\_D-5W & SVRB\_D-5W series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage range: ≤ 2:1;
- 2) 1.5KV input and output isolation;
- 3) Regulated and low ripple noise is required.

### SELECTION GUIDE

Model Number	Input Voltage(VDC)		Output Voltage (VDC)	Output Current (mA)		Input Current (mA)(typ.)		Reflected Ripple Current (mA,typ.)	Max. Capacitive Load <sup>#</sup> (μF)	Efficiency (% , typ.) @Max. Load
	Nominal (Range)	Max*		Max.	Min.	@Max. Load	@No Load			
SVRA1205D-5W	12 (9-18)	20	±5	±500	±50	521	13	50	1430	77
<del>SVRA1209D-5W</del>			±9	±278	±27	520	—		—	80
SVRA1212D-5W			±12	±210	±21	504	15		200	82
SVRA1215D-5W			±15	±166	±16	511	19		100	80
<del>SVRA1224D-5W</del>			±24	±105	±10	526	32		47	84
SVRB1205D-5W			5	1000	100	509	6		8570	78
SVRB1209D-5W			9	556	55	494	15		470	80
SVRB1212D-5W			12	420	42	500	17		288	82
SVRB1215D-5W			15	330	33	247	17		241	82
SVRB1224D-5W			24	200	20	471	23		68	83
SVRA2405D-5W	24 (18-36)	40	±5	±500	±50	252	4	150	727	80
<del>SVRA2409D-5W</del>			±9	±278	±27	242	8		503	82
SVRA2412D-5W			±12	±210	±21	245	10		330	83
SVRA2415D-5W			±15	±166	±16	246	9		247	83
SVRA2424D-5W			±24	±105	±10	248	12		68	84
SVRB2405D-5W			5	1000	100	258	6		2000	80
SVRB2409D-5W			9	556	55	241	6		388	82
SVRB2412D-5W			12	420	42	245	7		300	84
SVRB2415D-5W			15	330	33	247	8		538	84
SVRB2424D-5W			24	200	20	230	11		68	85
<del>SVRA4805D-5W</del>	48 (36-75)	80	±5	±500	±50	125	—	150	—	82
SVRA4809D-5W			±9	±278	±27	127	--		--	81
SVRA4812D-5W			±12	±210	±21	121	5		160	85
<del>SVRA4815D-5W</del>			±15	±166	±16	123	5		159	85
<del>SVRA4824D-5W</del>			±24	±105	±10	120	—		—	86
SVRB4805D-5W			5	1000	100	126	2		1780	82
<del>SVRB4809D-5W</del>			9	556	55	127	—		—	84
SVRB4812D-5W			12	420	42	126	10		617	85
<del>SVRB4815D-5W</del>			15	330	33	122	—		—	85
<del>SVRB4824D-5W</del>			24	200	20	124	—		—	86

Note: 1. Models listed with strike-through text have been officially discontinued.

2. \*Input voltage can't exceed this value, or will cause the permanent damage.

3. # For each output.

## INPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Input Surge Voltage (1000 ms)	12VDC Input Models	-0.7	--	25	VDC
	24VDC Input Models	-0.7	--	50	
	48VDC Input Models	-0.7	--	100	
Start-up Voltage	12VDC Input Models	--	--	9	VDC
	24VDC Input Models	--	--	18	
	48VDC Input Models	--	--	36	
Under Voltage Shutdown	12VDC Input Models	--	--	9	
	24VDC Input Models	--	--	18	
	48VDC Input Models	--	--	36	
Start-up Time	Nominal input& constant resistance load	--	300	--	ms
Short Circuit Input Power		--	0.5	--	W
No-load power consumption		--	500	--	mW
Input Filter				L Filter	

## OUTPUT SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Output Power		0.5	--	5	W
Positive voltage accuracy	Refer to recommended circuit	--	±1	±3	%
Negative voltage accuracy		--	±3	±5	
Output Voltage Balance	Dual Output, Balanced Loads	--	±0.5	±1	%
Line Regulation	Full load, Input voltage from low to high	--	±0.2	±0.5	
Load Regulation	10% to 100% load	--	±0.5	±2	
Transient Recovery Time	25%~50%~25% load or	--	300	500	μs
Transient Response Deviation	50%~75%~50% load step change	--	±3	±5	%
Temperature Drift	100% full load	--	±0.02	--	%/°C
Ripple*	20MHz Bandwidth	--	30	50	mVp-p
Noise*		--	100	150	
Over Current Protection	Full input voltage	120	--	--	%
Short Circuit Protection				Continuous, automatic recovery	

Note: 1. Dual output models unbalanced load: ±5%.  
2.\*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

## COMMON SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Tested for 1 minute and leakage current less than 1 mA	1500	--	--	VDC
Isolation Resistance	Test at 500VDC	500	--	--	MΩ
Isolation Capacitance	Input/Output, 100KHz/0.1V	--	1000	--	pF
Switching Frequency	Full load, nominal input	--	300	--	KHz
MTBF	MIL-HDBK-217F@25°C	1000	--	--	K hours
Case Material				Aluminous alloy	
Weight		--	15	--	g

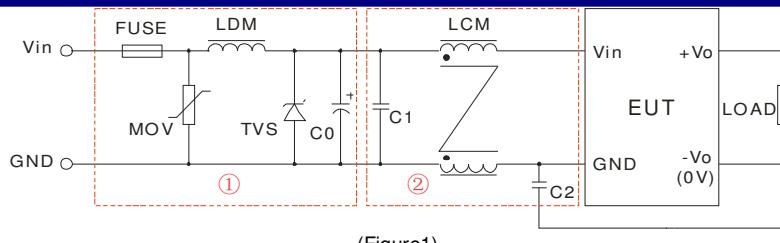
## ENVIRONMENTAL SPECIFICATIONS

Item	Test Conditions	Min.	Typ.	Max.	Unit
Storage Humidity	Non condensing	5	--	95	%
Operating Temperature	Power derating (above 71°C)	-40	--	85	°C
Storage Temperature		-55	--	125	
Temp. rise allowed at full load	Operating Temperature curve range	--	75	--	
Lead Temperature	1.5mm from case for 10 seconds	--	--	300	
Cooling				Free air convection	

## EMC SPECIFICATIONS

EMI	CE	CISPR22/EN55022 CLASS A (External Circuit Refer to Figure1-②)
EMS	ESD	IEC/EN61000-4-2 Contact $\pm 4\text{KV}$ perf. Criteria B
	EFT	IEC/EN61000-4-4 $\pm 2\text{KV}$ perf. Criteria B (External Circuit Refer to Figure 1-①)
	Surge	IEC/EN61000-4-5 $\pm 2\text{KV}$ perf. Criteria B (External Circuit Refer to Figure 1-①)

## EMC RECOMMENDED CIRCUIT



SVRA\_D-5W Recommended external circuit parameters:

Model		Vin: 12V	Vin: 24V	Vin: 48V	
EMS	FUSE	Choose according to practical input current			
	MOV	--	10D560K	10D101K	
	LDM	--	56 $\mu\text{H}$	56 $\mu\text{H}$	
	TVS	SMCJ28A	SMCJ48A	SMCJ90A	
	C0	680 $\mu\text{F}/25\text{V}$	120 $\mu\text{F}/50\text{V}$	120 $\mu\text{F}/100\text{V}$	
EMI	C1	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/100\text{V}$	
	LCM	9.5mH	9.5mH	9.5mH	
	C2	100pF/2KV	100pF/2KV	100pF/2KV	

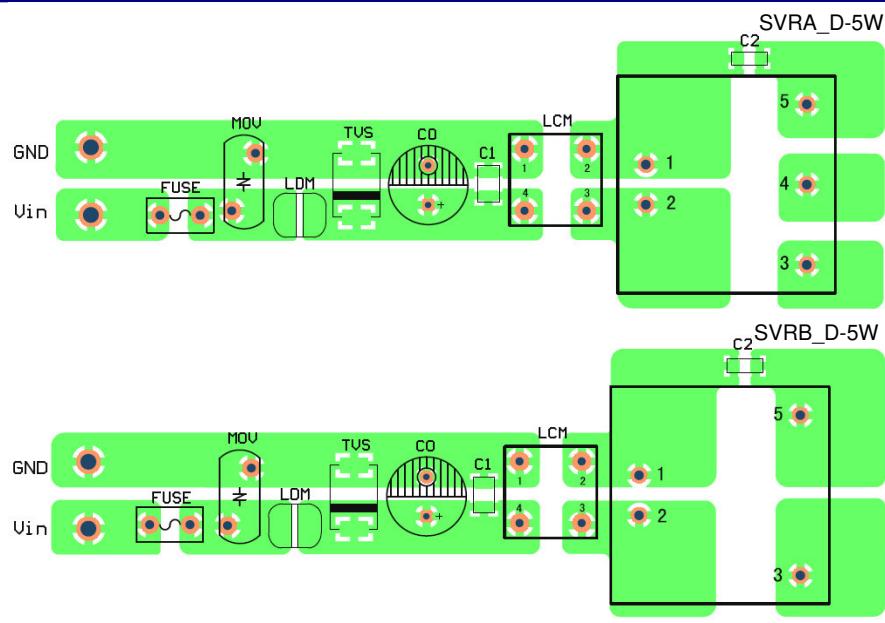
SVRB\_D-5W Recommended external circuit parameters:

Model		Vin: 12V	Vin: 24V	Vin: 48V	
EMS	FUSE	Choose according to practical input current			
	MOV	--	10D560K	10D101K	
	LDM	--	56 $\mu\text{H}$	56 $\mu\text{H}$	
	TVS	SMCJ28A	SMCJ48A	SMCJ90A	
	C0	680 $\mu\text{F}/25\text{V}$	120 $\mu\text{F}/50\text{V}$	120 $\mu\text{F}/100\text{V}$	
EMI	C1	--	4.7 $\mu\text{F}/50\text{V}$	4.7 $\mu\text{F}/100\text{V}$	
	LCM	--	1.27mH	9.5mH	
	C2	--	--	100pF/2KV	

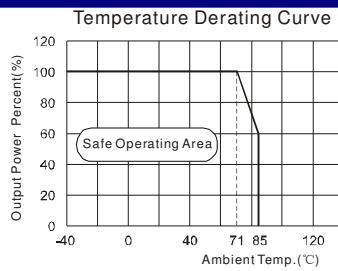
Note: 1. In Figure 1, part① is EMS Recommended external circuit, part② is EMI recommended external circuit. Choose according to requirements.

2. If there is no recommended parameters, the model no require the external component.

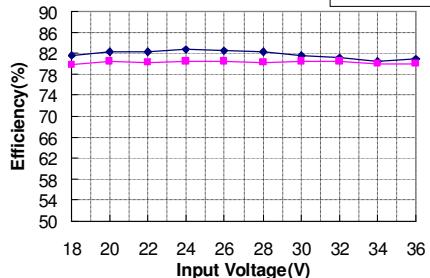
## EMC RECOMMENDED CIRCUIT PCB LAYOUT



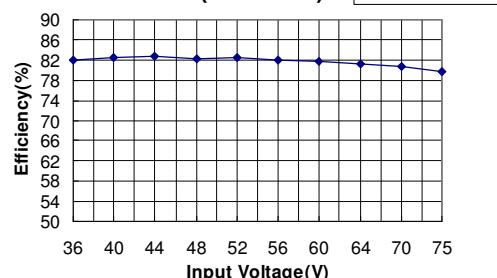
## PRODUCT TYPICAL CURVE



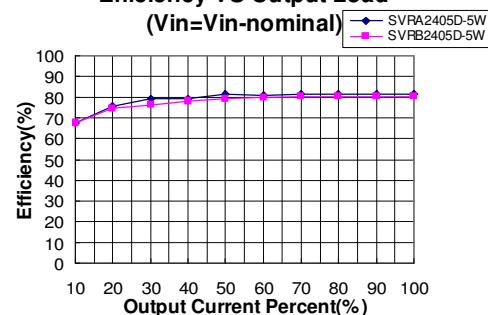
**Efficiency VS Input Voltage curve  
(Full Load)**



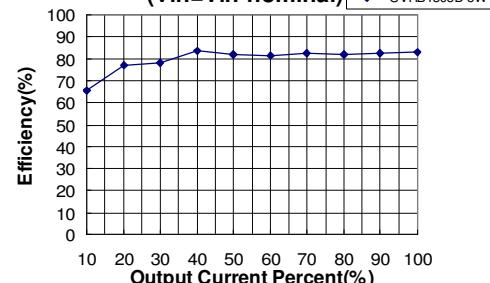
**Efficiency VS Input Voltage curve  
(Full Load)**



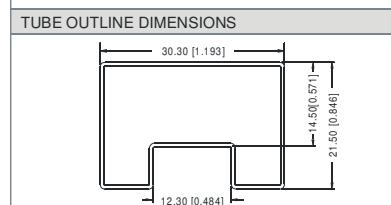
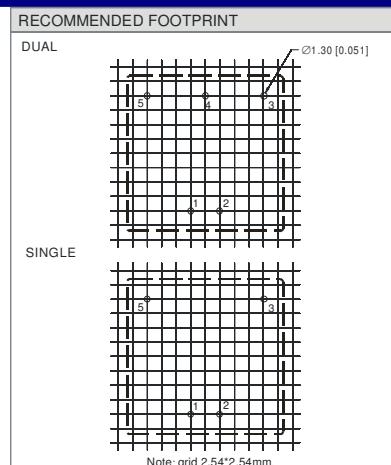
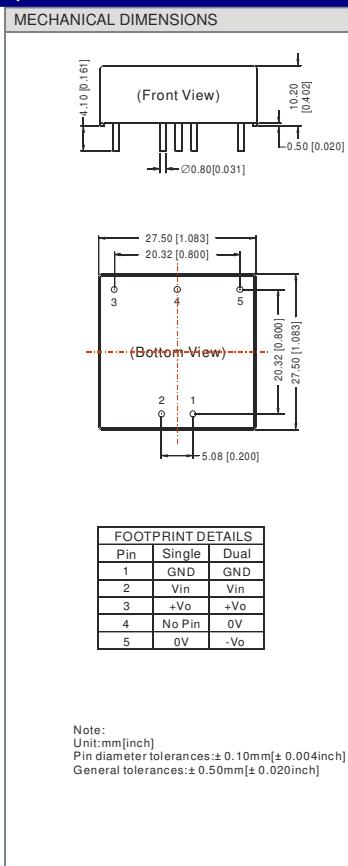
**Efficiency VS Output Load**



**Efficiency VS Output Load curve  
(Vin=Vin-nominal)**



## OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING

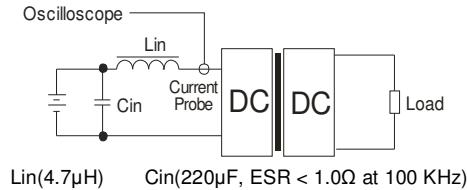


Note:  
Unit:mm[inch]  
General tolerances:  $\pm 0.50\text{mm} [\pm 0.020\text{inch}]$   
L=530mm[20.866inch] Tube Quantity: 19pcs  
L=220mm[8.661inch] Tube Quantity: 7pcs  
Short tube inner package dimensions: L'W'H= 255\*170\*80mm  
Short tube outer package dimensions (with six inner package boxes): L'W'H= 375\*280\*270mm  
Long tube inner package dimensions: L'W'H= 580\*200\*100mm  
Long tube outer package dimensions (with two inner package boxes): L'W'H= 600\*215\*220mm  
Long tube outer package dimensions (with three inner package boxes): L'W'H= 600\*215\*325mm

## TEST CONFIGURATIONS

### Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with a inductor Lin and Cin to simulate source impedance.



## DESIGN CONSIDERATIONS

### 1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load **could not be less than 10% of the full load**. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power.

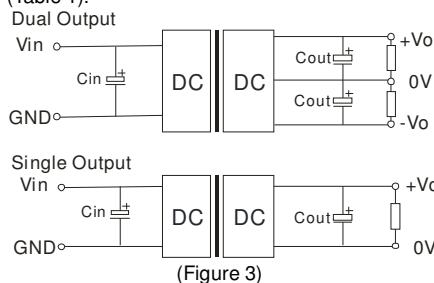
### 2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is add a circuit breaker to the circuit.

### 3) Recommended circuit

All the SVRA\_D-5W & SVRB\_D-5W Series have been tested according to the following recommended testing circuit before leaving factory. This series should be tested under load. Never be tested under no load (see Figure 3).

If you want to further decrease the output ripple, you can increase capacitance properly or choose capacitors with low ESR. However, the capacitance can't exceed the maximum capacitor load in the list (Table 1).



EXTERNAL CAPACITOR TABLE (TABLE 1)

Capacitance Output Voltage	Cout# (μF)	Cin(μF)
Single	5V	100
	12V,15V	
	24V	
Dual	±5V	100
	±12V,±15V	
	±24V	

Note: # For each output.

### 4) No parallel connection or plug and play

Note:

1. The load shouldn't be less than 10%, otherwise ripple will increase dramatically. Operation under minimum load will not damage the converter; However, they may not meet all specification listed, and that will reduce the life of product.
2. Max. Capacitive Load tested at nominal input voltage, full load and constant resistive load.
3. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
4. In this datasheet, all the test methods of indications are based on corporate standards.
5. Only typical models listed, other models may be different, please contact our technical person for more details.
6. Our company offer custom products.
7. Specifications subject to change without notice.